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This paper presents the design and implementation of the LTE-A downlink transmitter and receiver using a Field Programmable Gate Array (FPGA) according to release 10/11 on Virtex 6 XC6VLX240T FPGA...

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This paper presents a Field Programmable Gate Array (FPGA) design and implementation of the LTE downlink transmitter and receiver according to releases 8 and 9 on Virtex 6 XC6VLX240T FPGA kit using...

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FPGA architecture for the implementation of LTE downlink control channels in enviroMIMO nment. A brief out line of LTE downlink Control Channels is given in section 2; system model and its processing steps are explained in section the concept of 3; Alamouti's Space Frequency Block Codes is explained

FPGA IMPLEMENTATION OF 3GPP-LTE PHYSICAL DOWNLINK CONTROL ...

(PDF) Fast Implementation of Different LTE Physical Downlink Control Channels Using FPGA | Dr.heba abd el atty - Academia.edu Hardware implementation of LTE-advanced systems using FPGA technology is a highly promising technology for mobile communications and wireless network researchers.

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Design and implementation of linear precoding LTE downlink based on fpga Abstract: The problem that often occur in multi-antenna wireless communication is how to maximize the throughput. Precoding is a generalization of beamforming to support multi-layer transmission in multi-antenna wireless communications.

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paper presents a Field Programmable Gate Array (FPGA) design and implementation of the transmitter of the LTE downlink physical layer according to releases 8 and 9 on Virtex 6 XC6VLX240T FPGA kit using Xilinx® ISE® Design Suite version 12.1. General Terms SDR, LTE, 4G, 3GPP, OFDM, Transmitter , 2G ,3G, LTE

Software Defined Radio Implementation of LTE Transmitter

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FPGA implementation of 3GPP-LTE physical downlink control channel using diversity techniques. WSEAS Transactions on Signal Processing, 9 (2), 84-97.

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The frequency division duplex (FDD) mode is used in the downlink ... (PDF) Architecture and FPGA implementation of LTE PSS and ... Verifying an FPGA Implementation of an LTE Turbo Decoder - MATLAB and Simulink Tutorial. AuthorFPGA,MATLAB Simulink,Turbo Coding.

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The LTE application framework implements parts of a 3GPP-LTE release 10 compliant downlink and uplink physical layer transmitter and receiver. To keep the complexity of this application framework at a reasonably low level, only a subset of the physical layer features defined for 3GPP-LTE release 10 compliant devices is implemented.

LabVIEW Communications LTE Application Framework 1.1 White ...

An LTE downlink signal with a bandwidth of 1.4 MHz, modulated onto a 32 MHz IF carrier. The example measures signal quality at the output of the floating-point and fixed-point DDCs, and compares the two. Finally, FPGA implementation results are presented. ... HDL Code Generation and FPGA Implementation.

HDL Implementation of a Digital Down-Converter for LTE ...

LTE is accepted worldwide as the Long Term Evolution Perspective for today's 3G and 4G networks. The downlink physical channels of LTE include 3 data channels (PDSCH, PMCH and PBCH) and 3 control channels (PDCCH, PCFICH and PHICH). Control channels are inevitable for the transmission of both downlink and uplink data channels. Control information for one or multiple user equipments are ...

Implementation of physical downlink control channel (PDCCH ...

The LTE application framework implements parts of a 3GPP-LTE release 10 compliant downlink and uplink physical layer transmitter and receiver. To keep the complexity of this application framework at a reasonably low level, only a subset of the physical layer features defined for 3GPP-LTE release 10 compliant devices is implemented.

LabVIEW Communications LTE Application Framework 2.0 and 2 ...

The LTE-A technology increases the peak data rates to 1 Gbit/s in the downlink and to 500 Mbit/s in

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the uplink. LTE-A has several new features such as MIMO extensions (up to 4×4 for UL and up to 8×8 for DL), ... (GM), and by the fixed-point FPGA implementation of the proposed architecture. ...

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